

(19)

Europäisches Patentamt

European Patent Office

Office européen des brevets



(11)

EP 0 831 680 A1

(12)

EUROPEAN PATENT APPLICATION

published in accordance with Art. 158(3) EPC

(43) Date of publication:

25.03.1998 Bulletin 1998/13

(51) Int. Cl.⁶: H05H 1/46, H01L 21/205,

H01L 21/302, C23C 16/50,

C23F 4/00

(21) Application number: 97908538.8

(86) International application number:
PCT/JP97/01070

(22) Date of filing: 27.03.1997

(87) International publication number:
WO 97/36461 (02.10.1997 Gazette 1997/42)(84) Designated Contracting States:
DE FR GB• MURAKAMI, Tomomi
Sumitomo-Koto-Ryo(30) Priority: 28.03.1996 JP 73467/96
31.01.1997 JP 18912/97

Hyogo 660 (JP)

• TAKEDA, Naohiko

Hyogo 663 (JP)

• TSUYUGUCHI, Junya

Hyogo 663 (JP)

• KATAYAMA, Katsuo

Hyogo 661 (JP)

(71) Applicant:
SUMITOMO METAL INDUSTRIES, LTD.
Osaka-Shi, Osaka 541 (JP)(74) Representative:
Uchida, Kenji et al
Cabinet Beau de Loménie
158, rue de l'Université
75340 Paris Cedex 07 (FR)

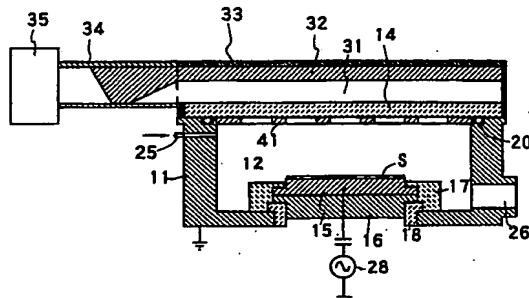
(72) Inventors:

- MABUCHI, Hiroshi
Hyogo 661 (JP)
- HAYAMI, Toshihiro
Hyogo 663 (JP)
- IDA, Hideo
Tokyo 198 (JP)

(54) DEVICE AND METHOD FOR PLASMA TREATMENT

(57) A plasma processing apparatus and plasma processing method are provided to be used for etching, ashing, CVD, etc. in the manufacturing, etc. of large-scale integrated circuits (LSIs) and liquid crystal display panels (LCDs). The plasma processing apparatus generates plasma by using a microwave introduced through a microwave window, while controlling the ions in the plasma by varying the RF voltage applied to the sample stage. The apparatus is characterized to include a counter electrode (grounded electrode) which is located at the rim section of the microwave window against the sample stage. The plasma processing method implements a plasma process for a sample with the plasma processing apparatus. The method and apparatus are capable of alleviating the sticking of particles to the sample and metallic contamination, and also capable of improving the yield of semiconductor devices, etc.

Fig. 1



Description

Technical Field of the Invention

The present invention relates to a plasma processing apparatus and plasma processing method used for etching, ashing, chemical vapor deposition (CVD), etc. in the manufacturing, etc. of large-scale integrated circuits (LSIs) and liquid crystal display panels (LCDs).

Background of the Invention

Processes based on plasma of reactive gases are adopted widely in the manufacturing of LSIs, LCDs, etc. Particularly, the dry etching technique using plasma is an indispensable fundamental technique for the manufacturing processes of LSIs, LCDs, etc.

On the other hand, as the size of substrates used for the fabrication of LSIs, LCDs, etc. increases, it is required to generate plasma uniformly over a wide area. It is also required for the dry etching technique and the embedding technique for thin film formation to control plasma generation and ion energy in the plasma independently.

The present applicant has disclosed in Japanese patent publication JP-A-Hei-6-104098 a plasma processing apparatus which is capable of generating plasma uniformly over a wide area and controlling the ion energy.

Fig. 1 is a brief vertical cross-sectional view of this plasma processing apparatus.

In the apparatus, a reaction room 12 has a microwave window 14 at the top, and a dielectric plate 32 for propagating the microwave is laid over the microwave window 14. A Radio frequency (RF) power source 28 is connected to a sample stage 15, which confronts a counter electrode 41 which is attached on the lower surface of the microwave window 14 by being grounded electrically.

Fig. 2 is a top view of an example of the counter electrode 41.

The counter electrode 41, which is made of metal such as aluminum, has microwave introduction holes 41a for introducing the microwave into the reaction room 12.

In the apparatus, the microwave is propagated flatly in the dielectric plate 32. Accordingly, it is possible for the apparatus to generate uniform plasma in a wide area by having a large dielectric plate 32 and a large microwave window 14.

Based on the disposition of the counter electrode 41 on the lower surface of the microwave window 14, it is possible for the apparatus, in the case of applying an RF voltage to the sample stage 15, to stabilize the electrical circuit including the sample stage, plasma and counter electrode, and generate a stable bias voltage on the surface of a sample S. Consequently, it is possible to control stably the acceleration of ion (ion energy)

in the plasma by varying the bias voltage.

The apparatus is designed to generate plasma mainly by using the microwave and control the ion energy mainly by using the RF voltage applied to the sample stage. Accordingly, it is possible to control plasma generation and ion energy independently.

However, the apparatus of the above-mentioned structure has its counter electrode located immediately above the sample. Because of this, the deposit is created in plasma and sticks to the counter electrode and the counter electrode itself is liable to be sputtered by the plasma and stick to the sample S. As a result, the apparatus is liable to suffer from the sticking of particles to the sample and also from metallic contamination.

The present invention is intended to resolve the foregoing problems, and its object is to provide a plasma processing apparatus and plasma processing method capable of reducing the sticking of particles to the sample and metallic contamination.

Summary of the Invention

The plasma processing apparatus of the present invention includes means for supplying a microwave, a reaction chamber having a microwave window and a sample stage which confronts the microwave window, means for applying a radio frequency (RF) voltage to the sample stage, and a counter electrode which is located to confront the sample stage and which is grounded electrically, wherein the counter electrode is located in the rim section of the microwave window inside the reaction chamber.

The apparatus of the present invention has its sufficiently large counter electrode located in the rim section of the microwave window, while avoiding the position immediately above the sample. Immediately above the sample, there is no metal that can be a source of particles or contamination. Therefore, it becomes possible to perform a plasma process with a stable bias voltage, while suppressing the sticking of particles to the sample and metallic contamination.

Brief Description of the Drawing Figures

- 45 Fig. 1 is a vertical cross-sectional view of the conventional plasma processing apparatus;
- Fig. 2 is a plan view of the counter electrode of the conventional plasma processing apparatus;
- Fig. 3 is a vertical cross-sectional view of a first embodiment of the plasma processing apparatus of the present invention;
- Fig. 4 is a detailed view of portion A of Fig. 3;
- Fig. 5 is a vertical cross-sectional view of the annular electrode as an example of the counter electrode;
- Fig. 6 is a bottom view of the annular electrode;
- Fig. 7 is a vertical cross-sectional view of a second embodiment of the plasma processing apparatus of

the present invention;

Fig. 8 is a detailed view of the microwave window and counter electrode of the apparatus of Fig. 7; Fig. 9 is a vertical cross-sectional view of an example of the counter electrode; Fig. 10 is a bottom view of the electrode of Fig. 9; Fig. 11 is a vertical cross-sectional view of a third embodiment of the plasma processing apparatus of the present invention; Fig. 12 is a vertical cross-sectional view of a fourth embodiment of the plasma processing apparatus of the present invention; Fig. 13 is a plan view of an example of the microwave regulation plate; Fig. 14 is a plan view of another example of the microwave regulation plate; Fig. 15 is a vertical cross-sectional view of a fifth embodiment of the plasma processing apparatus of the present invention; Figs. 16, 17 and 18 are diagrams showing the distribution of the rate of fluorocarbon polymer film formation on the wafer surface; and Fig. 19 is a graph showing the variation of the number of particles along the plasma discharge time.

Detailed Description of the Invention:

The rim section of the microwave window mentioned above signifies the outer portion of the microwave window around the central section immediately above the sample.

The bias voltage produced on the sample surface by the application of an RF voltage to the sample stage is generated through the electrical circuit including the sample stage, plasma and reaction chamber and therefore, the magnitude of the bias voltage is greatly affected by the plasma potential. The plasma potential varies greatly depending on the contact state between the electrically-grounded metal (which serves as the counter electrode) and the plasma.

Accordingly, in order for the apparatus, which is designed to generate high-density plasma beneath the microwave window, to control ion energy stably, the counter electrode must be located close to the position of plasma generation, i.e., close to the position under the microwave window.

Depending on the position of the counter electrode, the deposit, which is created in the plasma and which sticks to the counter electrode, and the counter electrode, itself, are both sputtered by the high-density plasma, resulting in the problems of sticking of particles to the sample and metallic contamination.

In dealing with this matter, the present inventors tried etching samples by varying the shape of the counter electrode which is located close to the position under the microwave window in pursuit of counter electrodes which alleviate the creation of particles and metallic

contamination.

The trial revealed: (a) the provision of an electrode that takes up only the rim section of microwave window which does not directly confront the sample stage, i. e., which is not immediately above the sample, can reduce the creation of particles and metallic contamination; (b) the electrode of only the rim section of the microwave window can be large enough to produce a stable bias voltage, provided that the reaction room is sufficiently large.

Namely, the apparatus of the present invention has a sufficiently large counter electrode located in the rim section of the microwave window, while avoiding the position immediately above the sample. Immediately above the sample, there is no metal that can be a source of particles or contamination. Therefore, it becomes possible to perform a plasma process with a stable bias voltage, while suppressing the sticking of particles to the sample and the metallic contamination.

This counter electrode is also advantageously raises the plasma density by limiting the microwave passage into the reaction room and thus, confining the plasma generation region. Increasing the plasma density not only raises the processing rate of the sample, but enhances the quality of etching of fine patterns.

Five embodiments of the plasma processing apparatus and plasma processing method based on the present invention and the results of experiments conducted with the plasma processing apparatus and plasma processing method of the present invention will be explained specifically.

First Embodiment

Fig. 3 shows in a vertical cross-section of the first embodiment of the plasma processing apparatus of the present invention.

The apparatus has a cuboidal hollow reaction chamber 11 which is generally made of metal such as aluminum or stainless steel. The reaction chamber 11 has a cylindrical reaction room 12 therein.

The top of the reaction chamber 11 is covered with a microwave window 14 so that the microwave window 14 airtightly seals the reaction room 12 by mean of an O-ring 20. The microwave window 14 is made of a dielectric substance, such as quartz glass (SiO_2) or alumina, having a high thermal durability, high microwave transmissivity, and small dielectric loss.

A sample stage 15 is disposed in the reaction room 12 at a position confronting the microwave window 14 and a sample S is placed on the sample stage 15. The sample stage 15 is connected to an RF voltage source 28 which is tuned to 400 kHz, 2 MHz, 13.56 MHz, etc.

The sample stage 15 has a chuck mechanism such as an electrostatic chuck (not shown) for holding the sample S and has a fluid circulating mechanism (not shown) for circulating a thermal medium for maintaining the sample S at a constant temperature. The sample

stage 15 is fixed to a base 16, for example, while being insulated from the reaction chamber 11 by an insulating member 18. The sample stage 15 is surrounded by a plasma shielding member 17.

In case the sample stage 15 is designed to have a function of an electrostatic chuck, it has its an aluminum electrode surface coated with alumina by implantation or similar. The insulating member 18 and plasma shielding member 17 are made of alumina or similar.

The reaction chamber 11 has a gas inlet hole 25 for feeding gas into the reaction room 12 and an evacuation port 26 which communicates with an evacuation device (not shown). The reaction chamber 11 may have a side wall equipped with a heater, etc. so that the reaction chamber 11 is heated to a prescribed temperature.

A dielectric plate 32 is disposed over the reaction chamber 11 to confront the microwave window 14 and the top of the microwave window 14 is covered with a metallic sheet 33. The dielectric plate 32 is connected to a microwave generator 35 through a waveguide 34. The dielectric plate 32 is made of a suitable material having a small dielectric loss, e.g., fluororesin such as Teflon (registered trademark of Dupont), polyethylene, or polystyrene. The microwave frequency adopted in the first embodiment is, for example, 2.545 GHz.

In addition to above-described arrangement the apparatus of the first embodiment is provided with an annular counter electrode 21 in the rim section of the microwave window 14 so that a bias voltage is produced stably on the surface of the sample S. The counter electrode 21 is grounded electrically through the reaction chamber 11, and the counter electrode 21 serves as the grounded electrode against the sample stage 15 to which the RF voltage is applied.

Fig. 4 shows the details of portion A of the apparatus of Fig. 3. The counter electrode 21 is separated from the side wall of reaction chamber 11 by the positioning of an electrode rim insulating plate 22 of alumina or similar which is fitted on the edge of the electrode 21.

Fig. 5 and Fig. 6 are a vertical cross-sectional view and bottom view, respectively of an annular electrode as an example of the counter electrode 21.

The counter electrode 21 is made of silicon (Si), aluminum (Al), etc. In the case of an aluminum counter electrode 21, the electrode surface is preferably rendered by, for example, the "alumite" process or alumina coating process.

For the uniform plasma processing of a sample, the counter electrode 21 is preferably shaped symmetrically with respect to the sample S. More preferably, it has an annular shape from the viewpoint of structural design of the apparatus, although this is not compulsory.

The method of plasma processing with the above-described apparatus for a sample S will be explained with reference to Fig. 3 by taking an example of the etching process for the surface of the sample S.

① The reaction room 12 is evacuated of air through

the evacuation port 26 and thereafter, gas is fed into the reaction room 12 through the gas inlet hole 25.

② The microwave generator 35 is activated to supply the microwave to the dielectric plate 32 through the waveguide 34. An electric field of surface wave is created in the hollow 31, and the electric field exerted across the microwave window 14 generates plasma in the reaction room 12.

③ Virtually simultaneously to plasma generation, the RF voltage source 28 is activated to apply an RF voltage to the sample stage 15, thereby producing a bias voltage on the surface of the sample S. The sample S is exposed to plasma, while ion energy of plasma is controlled by the adjustment of bias voltage, thereby etching the surface of the sample S.

Based on the provision of the counter electrode 21, which is shaped to exclude the portion immediately above the sample S, the sticking of particles to the sample S and metallic contamination can be reduced, and in addition the sample S can be etched based on a stabilized bias voltage produced on the surface of the sample S.

The counter electrode also advantageously raises the plasma density by limiting the microwave passage into the reaction room and thus, confining the plasma generation region. Increasing the plasma density not only raises the sample processing rate, but enhances the quality of etching of fine patterns.

Consequently, the plasma processing apparatus when used for the manufacturing of semiconductor devices is capable of improving the yield thereof.

Second Embodiment

Fig. 7 shows in a vertical cross-section of the second embodiment of the plasma processing apparatus of the present invention, and Fig. 8 shows the details of the microwave window and counter electrode of the apparatus.

The plasma processing apparatus differs from the apparatus shown in Fig. 3 only in the counter electrode 21. Specifically, the counter electrode 21 is structured to include part of the wall of the reaction chamber 11 of the apparatus shown in Fig. 3.

Fig. 9 and Fig. 10 are a vertical cross-sectional view and bottom view, respectively of the counter electrode.

The counter electrode 21 consists of a portion 21c, which functions as counter electrode against the reaction chamber 12, and of a portion 21d, which forms part of the wall of the reaction chamber 11. The counter electrode 21 is made of aluminum or similar, with an edge section 21a and a surface 21b that are exposed to plasma preferably being rendered by the "alumite" process or alumina coating process.

The apparatus of this structure of in which the coun-

ter electrode 21 and reaction chamber 11 are integrated, can reduce the creation of particles at the electrode fitting section. The counter electrode 21 is simpler in structure and therefore easier for manufacturing as compared with the counter electrode 21 of the first embodiment.

Third Embodiment

Fig. 11 shows a vertical cross-section of the third embodiment of the plasma processing apparatus of the present invention.

The edge section 21a of the counter electrode is liable to be sputtered by the high-density plasma generated immediately below the microwave window 14.

In the third embodiment, an annular ridge section 14b is formed on the microwave window 14 to protect the edge section 21a of the counter electrode against sputtering.

The structure of the third embodiment of the apparatus can suppress the increase with time of the sticking of particles to the sample S and metallic contamination.

Fourth Embodiment

Fig. 12 shows a vertical cross-section of the fourth embodiment of the plasma processing apparatus of the present invention.

The plasma processing apparatus, which is derived from the apparatus shown in Fig. 3, further includes a microwave regulation plate 23 lying on the microwave window 14.

Fig. 13 is a plan view of an example of the microwave regulation plate.

The microwave regulation plate 23, which is made of metal such as aluminum, has the formation of slits 23a at the central section which is above the sample S. The slits 23a, in a pattern configuration, regulates the electric field distribution of the microwave introduced into the reaction chamber 11, enabling the apparatus to perform more even plasma processing.

Fig. 14 is a plan view of another example of the microwave regulation plate.

The microwave regulation plate 23 has only one opening 23a in the central section, and it is adopted for coping with the unevenness of plasma processing between the inner and outer portions of the sample S.

The microwave regulation plate 23 can also be used for confining the plasma generation region. Specifically, it is intended to confine the strong plasma generation to the region above the sample S and reduce plasma generation in the outer region so as to alleviate the sputtering on the edge section 21a of the counter electrode. Namely, the microwave regulation plate 23 can also be used to suppress the increase with time of the sticking of particles to the sample and the metallic contamination.

Fifth Embodiment

Fig. 15 shows a vertical cross-section of the fifth embodiment of the plasma processing apparatus of the present invention.

The plasma processing apparatus has the microwave window 14 of the apparatus shown in Fig. 11 and the microwave regulation plate 23 of the apparatus shown in Fig. 12.

Based on the above-described structure, the apparatus can protect the edge section 21a of the counter electrode 21 against sputtering and confine the plasma generation region.

Consequently, it is capable of suppressing the increase with time of the sticking of particles to the sample and metallic contamination, and in addition it is capable of etching a sample S based on a stabilized bias voltage produced on the sample surface.

By the combination of the counter electrode 21, microwave window 14, and microwave regulation plate 23 having a proper shape of opening, it becomes possible to adjust the introduction of the microwave into the reaction chamber 11 to thereby increase the plasma density.

Consequently, the plasma processing apparatus of the present invention, when used for the manufacturing of semiconductor devices, is readily capable of improving the yield thereof.

As explained above, for generating a stable bias voltage, it is necessary for the apparatus to have a counter electrode 21 (grounded electrode) of a certain size or larger. The plasma processing apparatus of the present invention, with the arrangement of a counter electrode 21 at the rim of microwave window 14, can suitably have a large microwave window 14, and it is particularly suitable to include the dielectric plate 32 explained in the above-described first through fifth embodiments.

The plasma processing apparatus of the present invention is suitable for such a process as etching of silicon oxide (SiO_2) film in which control of ion is especially crucial.

The following explains the results of experiments conducted with the plasma processing apparatus of the present invention operating at a microwave frequency of 2.45 GHz and an RF voltage frequency of 400 kHz.

Experiment 1

The plasma processing apparatus, shown in Fig. 3, was used to etch continuously 25 pieces of 6-inch wafers on which a silicon oxide film of 1 μm in thickness is formed, and the number of particles of sizes of 0.2 μm or larger was counted on the entire surface. For comparison, the same experiment was conducted with the conventional plasma processing apparatus shown in Fig. 1.

Among the etching conditions, the flow rate of

CHF_3 was 20 sccm, the pressure was 30 mTorr, the microwave power was 1300 W, the RF voltage power was 1000 W, and the temperature of sample stage was 0 °C.

According to the plasma processing apparatus of the present invention, the particle count on the wafer was less than 50, as compared with several hundreds particles resulting from the conventional apparatus.

Particles on the wafer resulting from the plasma process of the apparatus of the present invention were analyzed with the EDX (energy dispersion X-ray analyzer). Aluminum (Al) and fluorine (F) were not detected simultaneously, and the reduction of particles coming out of the counter electrode where aluminum (Al) and fluorine (F) are to be detected was confirmed.

Experiment 2

The plasma processing apparatus shown in Fig. 12 was used to assess the ability of improving the uniformity of plasma by the microwave regulation plate.

The uniformity of plasma was assessed in terms of the distribution of the rate of fluorocarbon polymer formation on a 6-inch silicon wafer resulting from the decomposition of CHF_3 gas by plasma.

The microwave regulation plate, shown in Fig. 14, which limits the microwave passage to a rectangular area, and the microwave regulation plate shown in Fig. 15, which adjusts the electric field distribution of the microwave, were used. For comparison, the same measurement was conducted with the apparatus having no microwave regulation plate.

Among the etching conditions, the flow rate of CHF_3 was 20 sccm, the pressure was 30 mTorr, the microwave power was 1300 W, and the temperature of sample stage was 0 °C. No RF voltage was applied to the sample stage.

Fig. 16, Fig. 17 and Fig. 18 show the distribution of the rate of fluorocarbon polymer formation on the wafer surface. Fig. 16 is the case with the microwave regulation plate having the opening shown in Fig. 15. Fig. 17 is the case with the microwave regulation plate having the opening shown in Fig. 14. Fig. 18 is the case without a microwave regulation plate. The unit of polymer forming rate is nm/min. Symbols "+" and "-" in Fig. 16 and Fig. 17 signify that the etching rate is 400 nm/min or higher and lower than 400 nm/min, respectively.

It was confirmed that by the provision of the microwave regulation plate, the distribution of film forming rate is made uniform, and plasma of uniform density distribution can be generated.

Experiment 3

The plasma processing apparatus shown in Fig. 15 having the microwave regulation plate shown in Fig. 14 was used to generate plasma and measure the variation with time of the number of particles of sizes of 0.2

μm or larger on the entire surface of a 6-inch wafer. For comparison, the same measurement was conducted with the plasma processing apparatus having a flat microwave window 14 without the annular ridge section 14b.

Among the plasma generation conditions, gases used were CHF_3 and CO, the microwave power was 1300 W, and the RF voltage power was 600 W.

Fig. 19 shows a graph of the variation of particle count with respect to the plasma discharge time.

The apparatus having the microwave window without the annular ridge section 14b maintained the particle count below 50 in the first 1000 mins at most, whereas the apparatus having the microwave window with the annular ridge section 14b maintained the particle count below 50 for longer than 2500 mins.

It was confirmed that by the formation of the microwave window so as to protect the edge section of the counter electrode, the increase in the number of particles that stick to the sample with time can be suppressed further.

Industrial applicability

As described above, the plasma processing apparatus and plasma processing method of the present invention are capable of alleviating the problems of the sticking of particles to the sample and metallic contamination, and are also capable of etching a sample based on a stabilized bias voltage produced on the sample surface. Moreover, the method and apparatus of the present invention are capable of performing the uniform plasma processing for samples having large areas.

Consequently, the plasma processing apparatus and plasma processing method of the present invention, when used for the manufacturing of large-scale integrated circuits (LSIs) and liquid crystal display panels (LCDs), are capable of improving the yield thereof.

Claims

1. A plasma processing apparatus comprising:

means for supplying a microwave;
a reaction chamber having a microwave window and a sample stage which confronts said microwave window;
means for applying an RF voltage to said sample stage; and
a counter electrode which is located to confront said sample stage and which is grounded electrically, wherein said counter electrode is located in a rim section of said microwave window inside said reaction chamber.

2. The plasma processing apparatus according to claim 1, further comprising a dielectric plate for propagating said microwave, said dielectric plate

being disposed between said microwave supply means and said microwave window to confront said microwave window.

3. The plasma processing apparatus according to claim 1 or 2, wherein said microwave window is shaped such that an edge section of said counter electrode is protected. 5
4. The plasma processing apparatus according to any of claims 1 through 3, further comprising a metallic microwave regulation plate having an opening for introducing said microwave, said microwave regulation plate being disposed close to said microwave window on said outer side of said reaction chamber. 10 15
5. A plasma processing method for implementing a plasma process for a sample with a plasma processing apparatus as set forth in any of claims 1 through 4. 20

25

30

35

40

45

50

55

Fig. 1

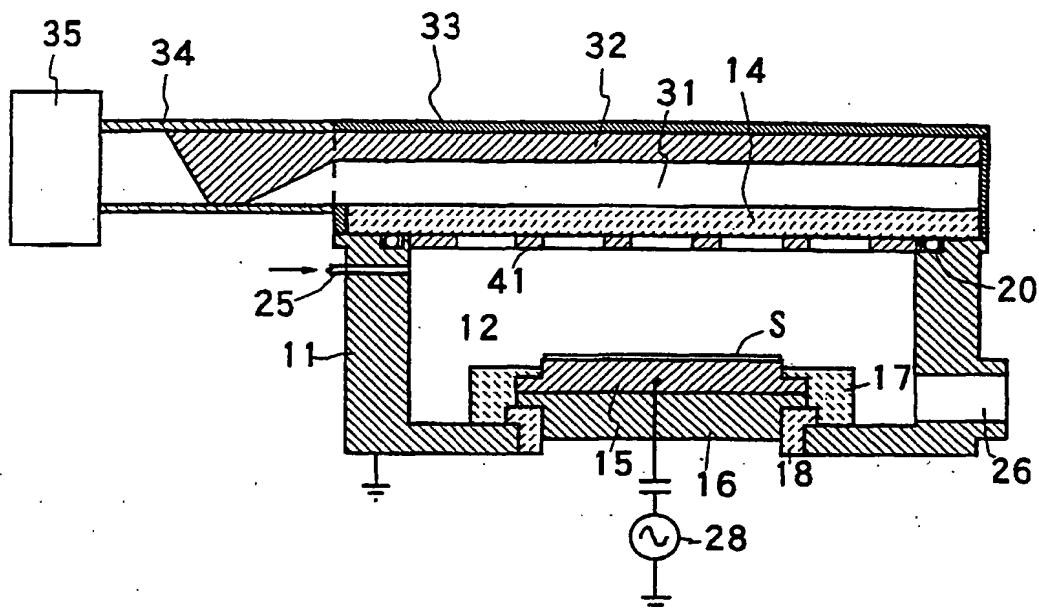


Fig. 2

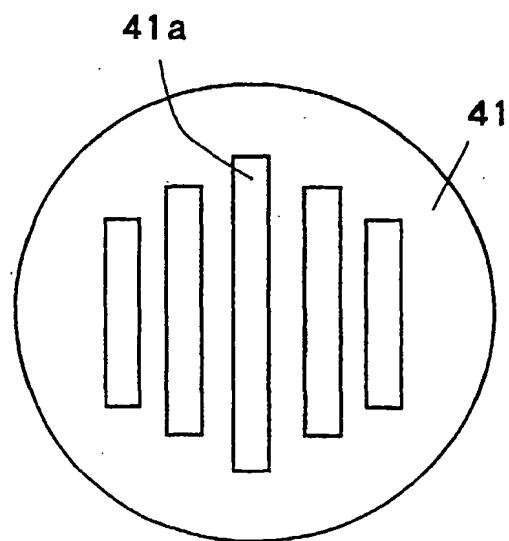


Fig. 3

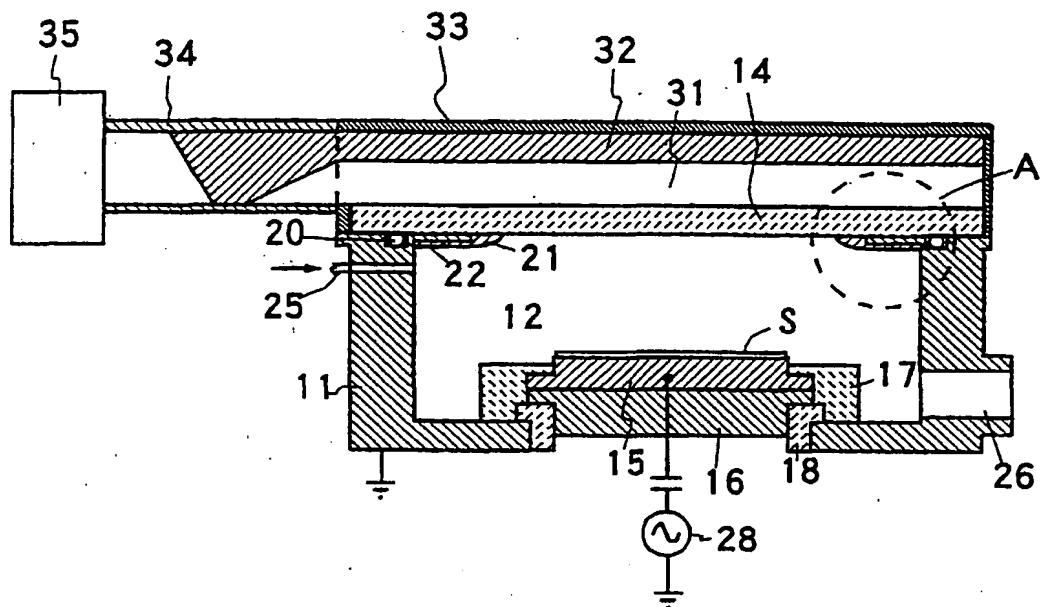
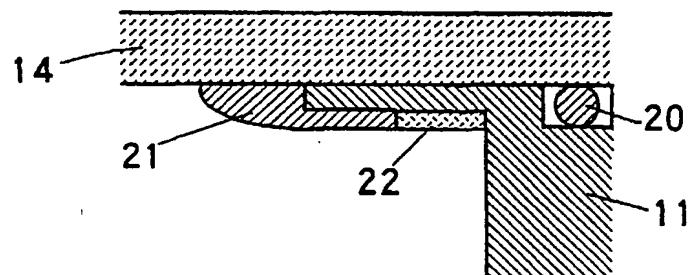


Fig. 4



EP 0 831 680 A1

Fig. 5

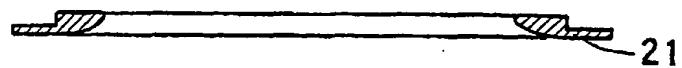


Fig. 6

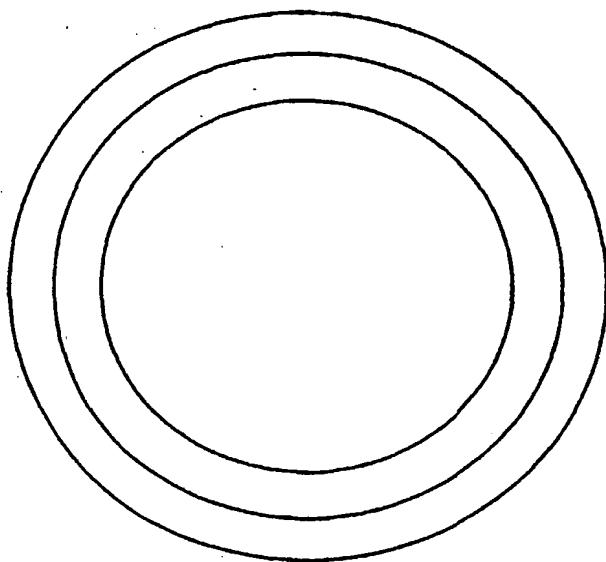


Fig. 7

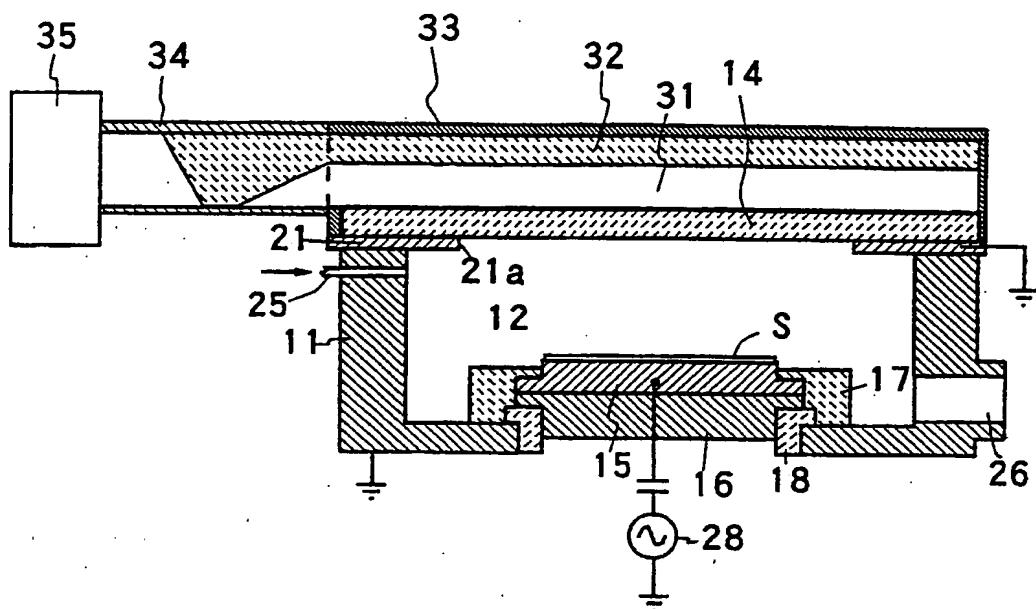


Fig. 8

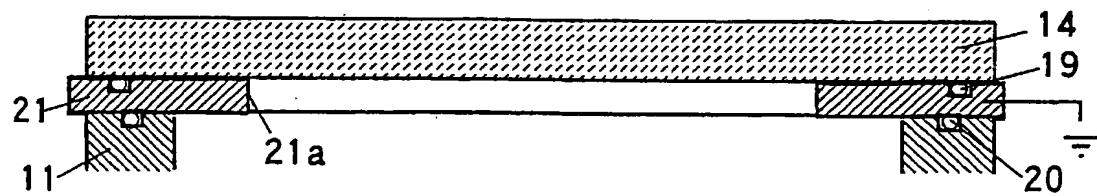


Fig. 9

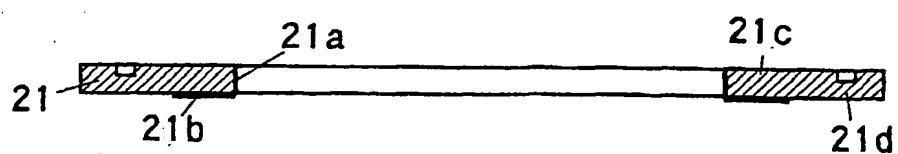


Fig. 10

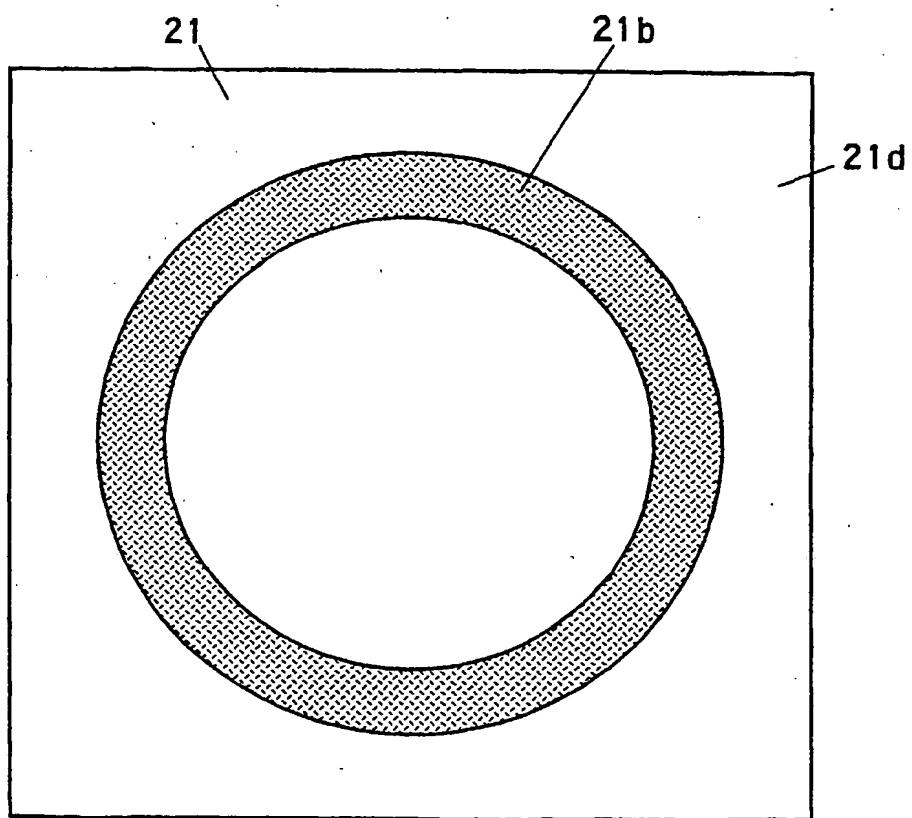


Fig. 11

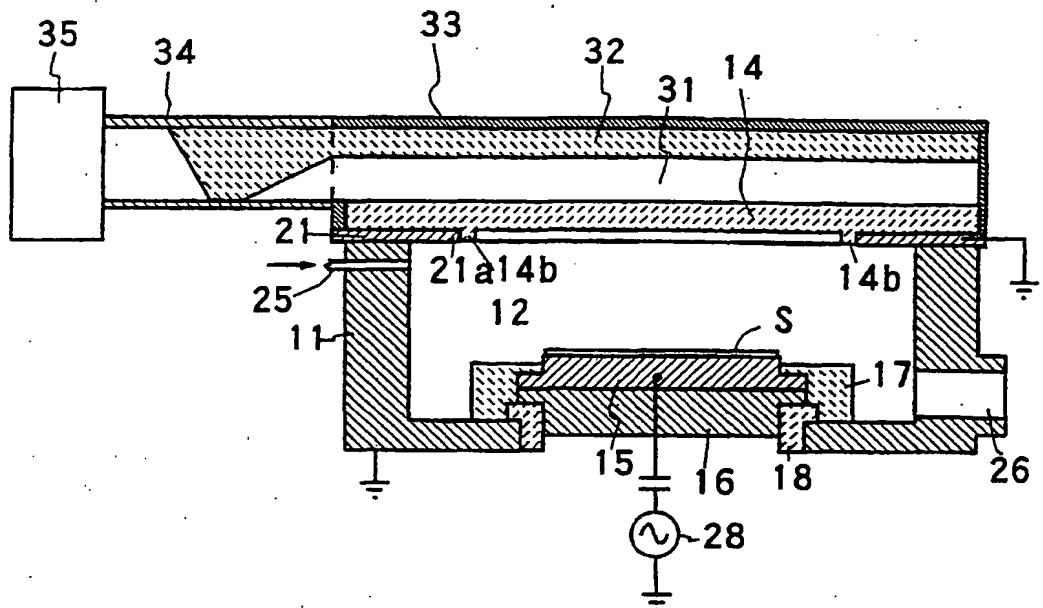


Fig. 12

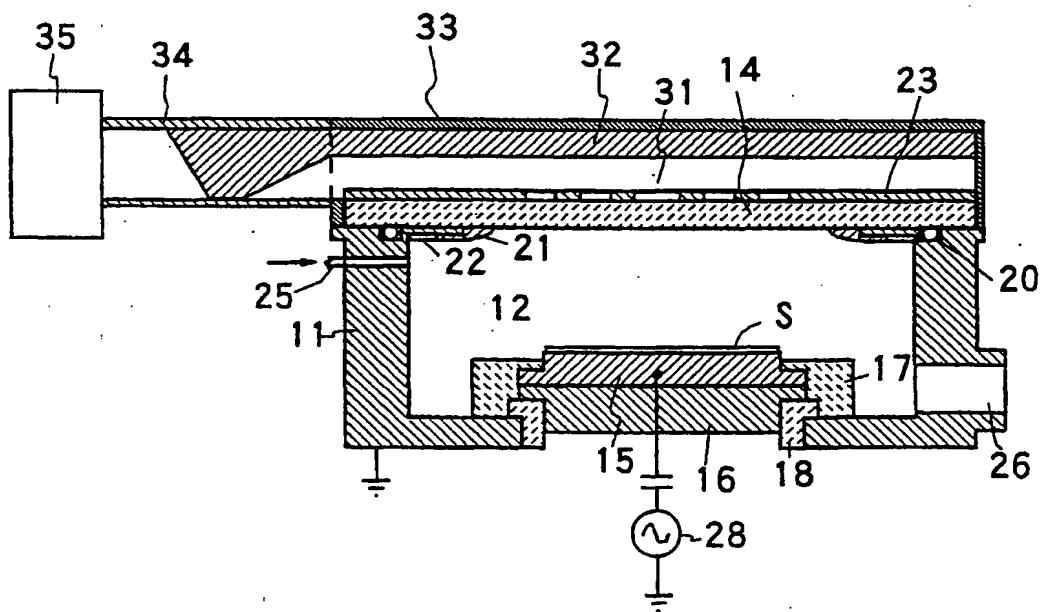


Fig. 13

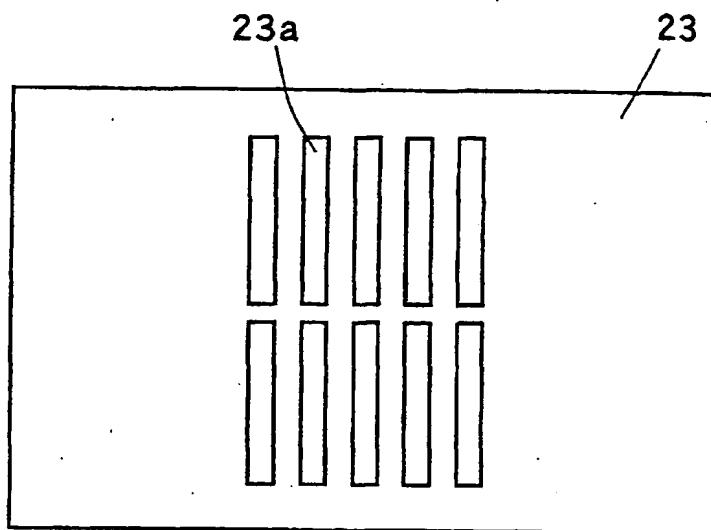


Fig. 14

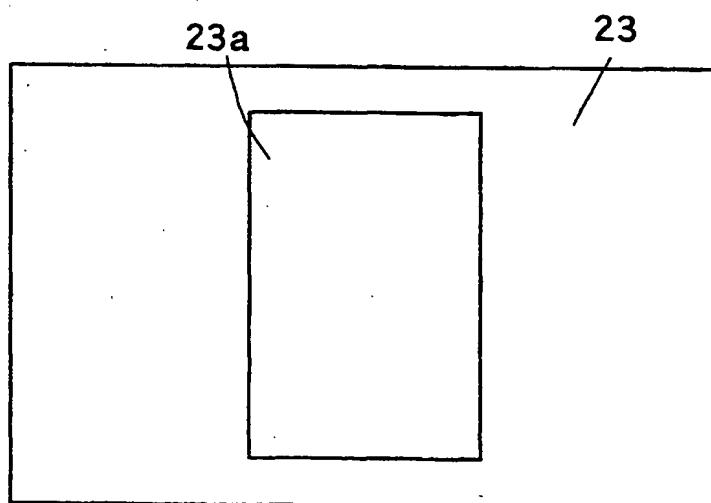


Fig. 15

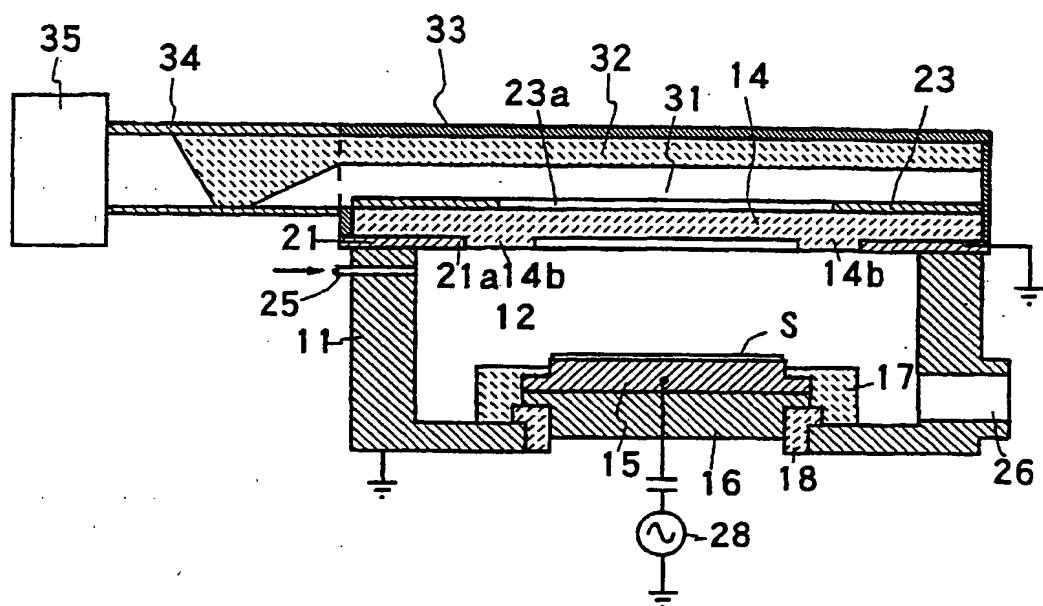


Fig. 16

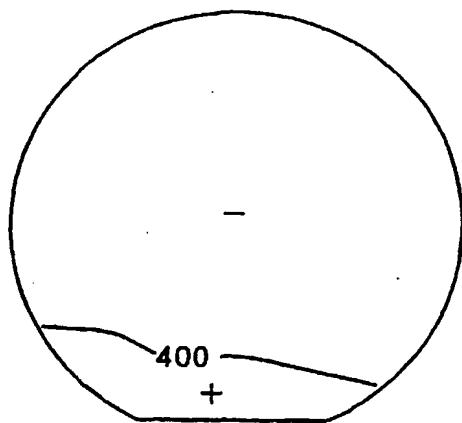


Fig. 17

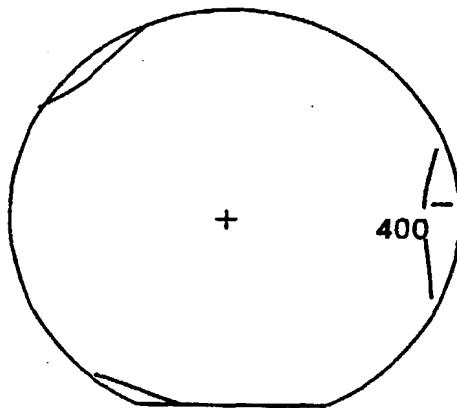


Fig. 18

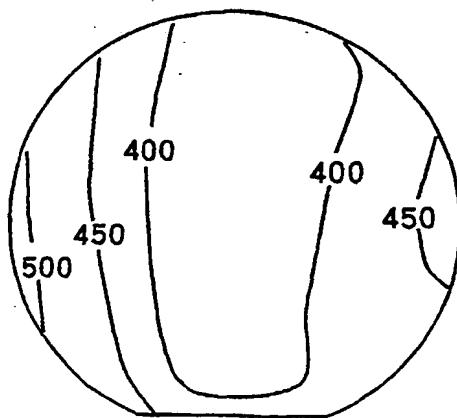
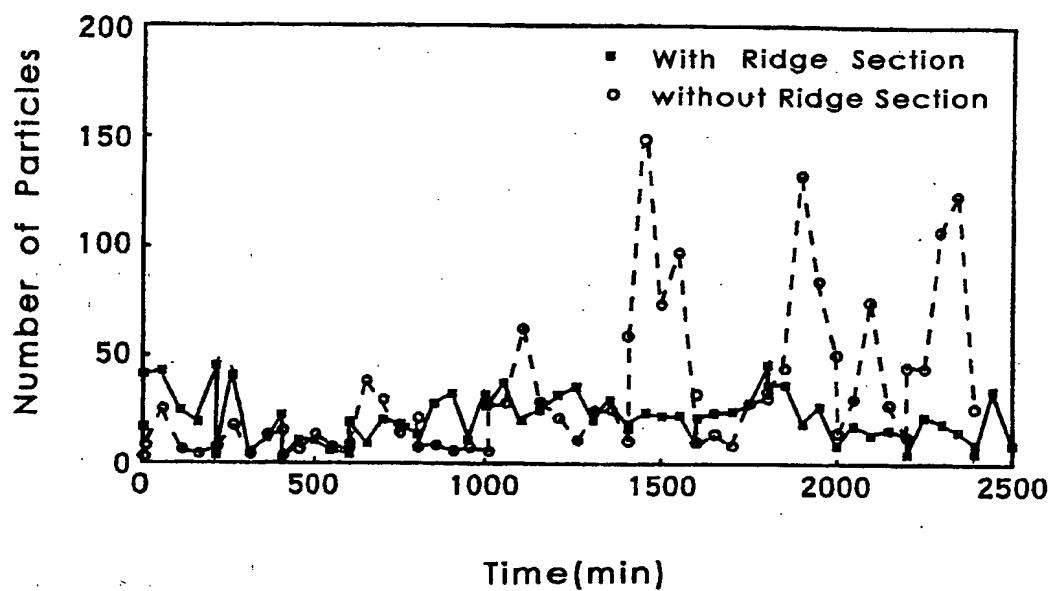


Fig. 19



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP97/01070

A. CLASSIFICATION OF SUBJECT MATTER Int. C1 ⁶ H05H1/46, H01L21/205, 21/302, C23C16/50, C23F4/00 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int. C1 ⁶ H05H1/46, H01L21/205, 21/302, C23C16/50, C23F4/00		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1926 - 1996 Jitsuyo Shinan Toroku Kokai Jitsuyo Shinan Koho 1971 - 1997 Koho 1996 - 1997 Toroku Jitsuyo Shinan Koho 1994 - 1997		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP, 4-117437, U (Sumitomo Metal Industries, Ltd.), October 21, 1992 (21. 10. 92) (Family: none)	1 - 5
A	JP, 7-335567, A (Sumitomo Metal Industries, Ltd.), December 22, 1995 (22. 12. 95) (Family: none)	1 - 5
A	JP, 6-112138, A (Sumitomo Metal Industries, Ltd.), April 22, 1994 (22. 04. 94) (Family: none)	1 - 5
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
<p>* Special categories of cited documents:</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubt on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p>		<p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</p> <p>"&" document member of the same patent family</p>
Date of the actual completion of the international search June 24, 1997 (24. 06. 97)		Date of mailing of the international search report July 1, 1997 (01. 07. 97)
Name and mailing address of the ISA/ Japanese Patent Office Facsimile No.		Authorized officer Telephone No.

Form PCT/ISA/210 (second sheet) (July 1992)